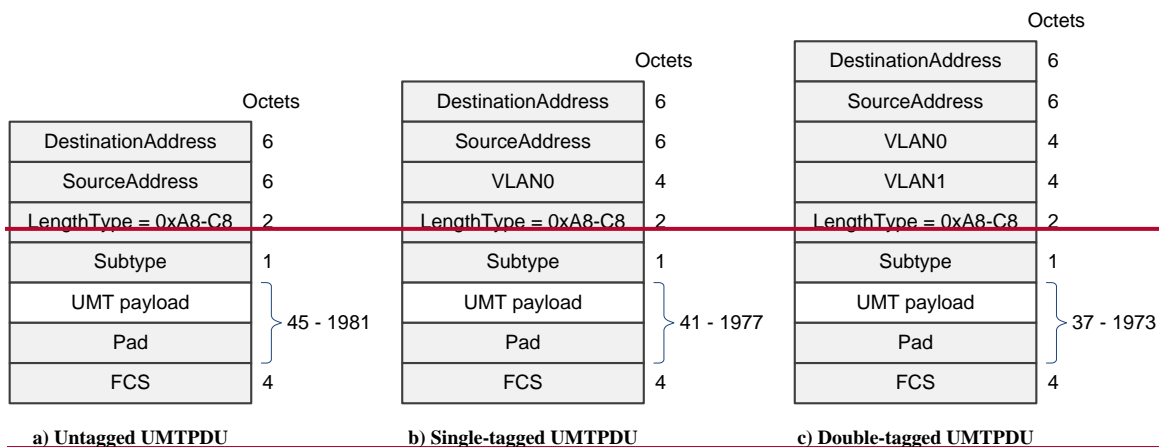
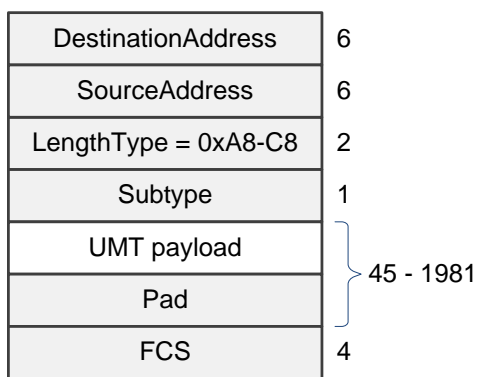


1



2

Octets



3

4

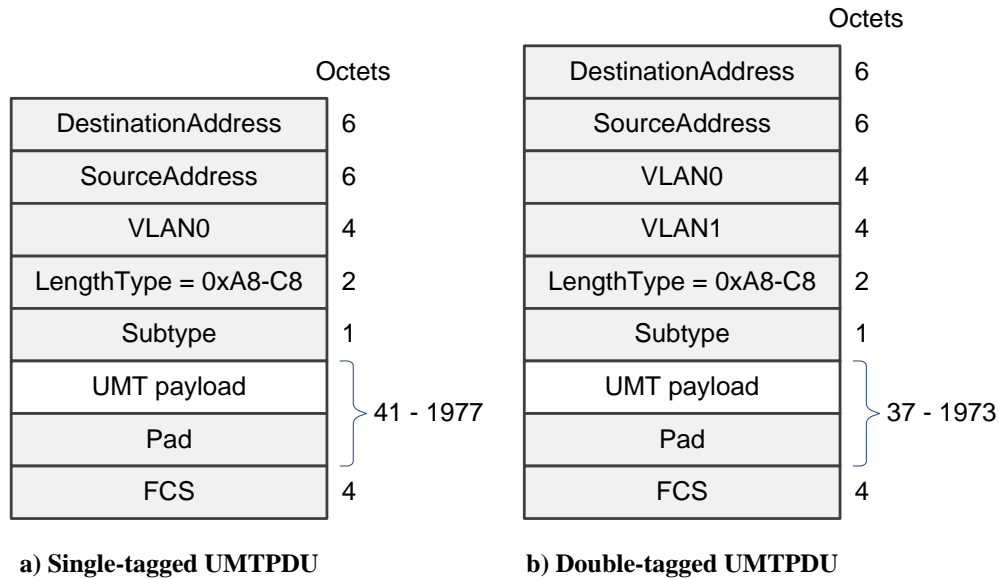
Figure 5-1—UMTPDU format

5

### 5.3 VLAN-Tagged UMT PDU

~~Editorial Note (to be removed prior to publication): Glen took an AI to generate content for this particular section, including addressing comment #6 from D0.4, and submit via comment on D0.5.~~

All UMT PDU subtypes defined in 5.2.1 through 5.2.6 may include one or two VLAN tags. If a single VLAN tag is used as part of UMT PDU header, the maximum allowed UMT payload size is reduced by 4 octets. If two VLAN tags are used, the maximum UMT payload size is reduced by 8 octets. The format of single-tagged and double-tagged UMT PDUs is shown in Figure 5-6.



**Figure 5-2—Single-tagged and double-tagged UMT PDU format**

Operations on VLAN-tagged UMT PDUs are described in 6.4.

## 6.4 CTE rules involving operations on the VLAN tags

The classification clauses in the CTE rules may classify the incoming xPDUs and UMT PDUs based on VLAN0 or VLAN1 fields, or based on some sub-fields of these fields (see Table 6-2).

The action clauses in the CTE rules may add VLAN0 and VLAN1 tags to UMT PDUs or delete these tags from UMT PDUs. When performing a translation of an xPDU into an UMT PDU, and if the original xPDU includes any VLAN tags, the action clauses may also copy these tags from xPDU into UMT PDU. The COPY operation leaves the VLAN tags in the original xPDU intact.

Even though the UMT sublayer may be configured to manipulate VLAN tags in UMT PDUs, it does not imply that a given UMT-aware device is also VLAN-aware and that it is a participant in Multiple VLAN Registration Protocol (MVRP). The VLAN manipulation applied by the UMT sublayer is entirely based on the provisioned CTE rules and not on any higher-layer protocol behavior or device configuration. In a VLAN-enabled L2 network, the management entity responsible for UMT port configuration and provisioning is expected to be aware of VLAN topology and to participate in MVRP if necessary.