

IEEE 1904.3 RoE TF Closing Report

December 15-17, 2015

Chair: Jouni Korhonen, BRCM

Co-chair: Liquan Yuan, ZTE

Editor: Richard Maiden, Altera

Input documents

- Total 13 input papers (excluding updates):
 - 2 discussion papers
 - 10 technical contributions for the baseline
 - 1 late contribution was not presented

AIS

- OPEN Shijia Ma: write a proposal how to handle silent suppression. -> propose to drop (we had already a presentation on silent suppression from PMCS).
- OPEN Shijia Ma: write a proposal on handling sequence numbers and timestamps. How to detect packet loss in a case of timestamps. -> propose to drop since TF has made decision where this is not topical anymore.
- OPEN Jinri Huang: Manageability of RoE endpoints, alarms, etc.
- CLOSED Jouni: ask permission to distribute NGFI slides used in ComSoc/SDB.

Motions

□ Total 7 motions and 7 approved.

- Approve as the baseline proposal the RoE header 32-bit ordering info field timestamp representation, semantics and example algorithm as described in tf3_1512_tse_timestamp_format_num_2.pdf page 3 option 2 and tf3_1512_tse_timestamp_format_draft_change_2 .docx.
- Raz Gabe making the motion
- Seconded by Jouni Korhonen
- \square Technical motion (>=2/3)
- ☐ Yes: 8, no: 0, abstain 0

Approve as the baseline proposal the RoE header 32-bit ordering info field sequence number representation, semantics and example algorithm as described in tf3_1512_korhonen_seqnum_draft_change_1.docx.

- Jouni Korhonen making the motion
- Seconded by Richard Maiden
- \square Technical motion (>=2/3)
- ☐ Yes: 8, no: 0, abstain 0

- Approve as the baseline for the CPRI mapper payload handling the following:
- □ Describe structure aware and agnostic mappers separately so that they do not necessarily share the same "description language".
- Structure agnostic mapper shall
 - have only two configuration variables: number of CPRI basic frames in a RoE packet and flowID.
 - generate only one flow.
- Structure aware mapper shall support the "flexible container functionality" as described in tf3_1512_korhonen_mapper_draft_change_2.docx with the following clarifications:
 - "reserved" NIL flowID determines when a container is sent to the "control process".
 - The stuffing bits are not supported (CPRI mapping method #1).
 - Sample interleaving is explicitly described per mapper, no separate variable.
 - .lenSample length is not required anymore.
 - Each "container" shall be associated with a single flowID.
 - Modulo operation is applied to maximum 256 basic frames and the modulo value shall be such that it wraps on each 10ms radio frame.
 - The "RoE.segment.num" describes the number of collected container sets.
- Jouni Korhonen making the motion
- Seconded by Richard Maiden
- Technical motion (>=2/3)
- Yes: 7, no: 0, abstain 0

- Approve as a baseline for the CPRI structure aware mapper control word handling as outlined in tf3_1512_korhonen_cw_1.pdf and tf3_1512_korhonen_cw_draft_change_1.docx with the following clarifications:
 - The maximum number of "containers" is 8.
 - Both Ctrl AxC and VSD use the same container definition structure.
 - Remove .cw_size as it is implicit to different mapper uses.
 - Remove .filter_mode "content has changed since the previous..".
 - "container" definitions are not used for Fast & Slow C&M but they will have simplified use case specific definitions.
 - "containers" may overlap from CPRI->RoE mapping direction.
 - "containers" should not overlap from RoE->CPRI direction (if they do the outcome is undefined).
 - offset is relative to the extracted container defined content.
 - Byte ordering is network byte order within a control word and control words follow the order they are received in basic frames when placed into a RoE packet.
 - Modulo operation does not have history of previously received data i.e., modulo cannot be used to collect data from multiple basic frames.
- Jouni Korhonen making the motion
- Seconded by Yasser Bajwa
- Technical motion (>=2/3)
- Yes: 8, no: 0, abstain 0

- □ Approve adding a payload length field into the RoE header as described in tf3_1512_bross_length_2.pdf page 4 with a change that the length field includes only the payload length. The payload length counts octets following the common header "ordering info" field in a RoE packet.
- Kevin Bross making the motion
- Seconded by Jouni Korhonen
- \square Technical motion (>=2/3)
- Yes: 8, no: 0, abstain 0

- Agree on Hierarchy on p3-5 of tf3_1512_maiden_control_hierachy_3.pdf
- Agree on parameter priority p6 of tf3_1512_maiden_control_hierachy_3.pdf
- Richard Maiden making the motion
- Seconded by Yasser Bajwa
- \square Technical motion (>=2/3)
- ■Yes: 8, no: 0, abstain: 0

- □ Agree on baseline proposal for Link parameters p8, flow parameters p9, simple mapper parameters p11 and structure agnostic mapper parameters p12 tf3_1512_maiden_control_hierachy_3.pdf
- Richard Maiden making the motion
- Seconded by Raz Gabe
- \square Technical motion (>=2/3)
- ■Yes: 8, no: 0, abstain: 0

Other discussions

- □ Update of NGFI (to be IEEE 1914)
 - Status, scope and PAR
- RoE tunneling mapper
 - Background, realization, management, ...

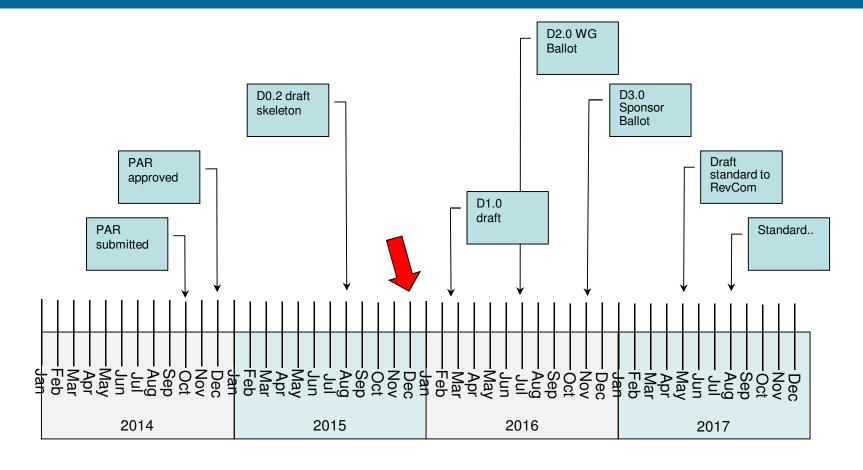
About the 1904.3 TF timeline

- Since we are still in pre-D1.0 phase current timeline we had just does not hold (D1.0 by the end of 2015).
 - Stop bringing in new "stuff" and get the D1.0 out.
 - Proposal not to work on control protocol for configuring nodes etc.. assume manual configuration.

☐ The PAR says:

- 4.2 Expected Date of submission of draft to the IEEE-SA for Initial Sponsor Ballot: 11/2016
- 4.3 Projected Completion Date for Submittal to RevCom: 05/2017
- This means _we_are_in_hurry_ if the same pace remains!

Adjustments of the 1904.3 Timeline



■ We are behind the schedule by ~one meeting cycle!



Done